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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\* *JmJ*  
 None

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *JmJ*  
 None

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>Jonice M. Thomas</i> Examiner's Signature Initials	STATE OR COUNTRY TAIWAN	SHEETS DRAWING 2	TOTAL CLAIMS 33	INDEPENDENT CLAIMS 2
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TITLE  
 Method of forming dual gate insulator layers for CMOS applications

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